

Substitute for Form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Complete if Known	
				Application Number	09/724,585
				Filing Date	11/28/2000
				First Named Inventor:	John M. Beardslee
				Art Unit	2123
				Examiner Name	Thangavelu, K.
Sheet	1	of	7	Attorney Docket No.	02986.P035

U.S. PATENT DOCUMENTS						
Exmnr Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (If known)			
A		US- 6,587,995	B1	07/01/2003	Duboc et al.	
		US- 6,779,145	B1	08/17/2004	Edwards et al.	
		US- 6,785,854	B1	08/31/2004	Jaramillo et al.	
I		US- 6,789,217	B2	09/07/2004	Slaugh et al.	
		US- 6,791,352	B2	09/14/2004	Verdoorn et al.	
		US- 6,795,963	B1	09/21/2004	Andersen et al.	
K/M/N		US- 6,799,128	B2	09/28/2004	Duff et al.	
		US- 6,822,474	B2	11/23/2004	Chaudhari	
		US- 6,823,224	B2	11/23/2004	Wood et al.	
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		US- 6,839,874	B1	01/04/2005	Fang	
		US- 6,859,892	B2	02/22/2005	Bolding et al.	
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Exmnr Initials*	Cite No. ¹	Foreign Patent Document			Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³	Number ⁴	Kind Code ⁵ (if known)				
↑ <i>KRM</i> ↓		WIPO	00/043884	A1	07/27/2000	Wenzel et al.		
		WIPO	01/063434	A1	08/30/2001	Barry		
		WIPO	01/071876	A1	09/27/2001	Ricchetti et al.		

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		US- 6,904,577 B2	06/07/2005	Schubert et al.	
↓		US- 2004/0111252 A1	06/10/2004	Burgun et al.	
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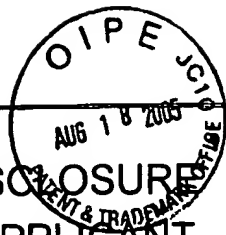
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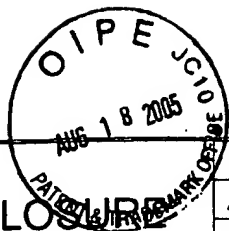


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		"Actel Corporation, "Actel Silicon Explorer II - User's Guide", March 2004; cover page, page ii and pp.3-43.			
KM		Altera Corporation, "Quartus II Handbook, Volume 3", Section IV-1, May 2005; pp. 9/1-9/14; pp. 10/1-10/46; pp. 11/1-11/46; pp. 12/1-12/10.			
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NON PATENT LITERATURE DOCUMENTS

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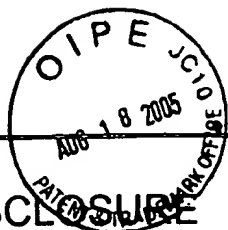
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↑		First Silicon Solutions, Inc., "Technical Data for System Navigator for AMD Geode GX Processor", http://www.fs2.com , June 2005, 2 pgs.	
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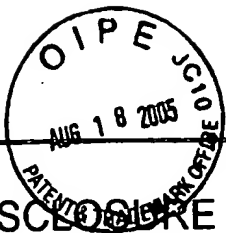
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		Stollon et al., "Multi-Core Embedded Debug for Structured ASIC Systems", DesignCon 2004, February 2004, pp. 1-23.	

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KMN		Yang et al., "Extraction Error Modeling and Automated Model Debugging in High-Performance Low Power Custom Designs", Date Conference 2005, March 2005, 6 pgs.			

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